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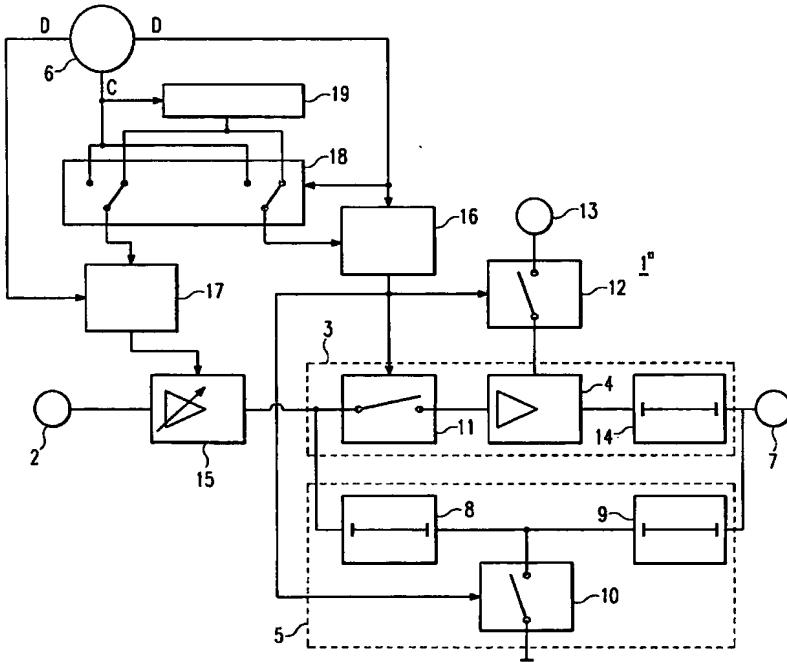
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(54) Title: GLITCH-FREE CONTROLLABLE RF POWER AMPLIFIER



(57) Abstract: The present invention proposes a power amplifier circuit for amplifying an input RF signal with respect to a specified RF output power comprising an input terminal (2) for supplying the input RF signal to be amplified, an output terminal for the RF signal with the output power specified, an amplification path (3) formed between the input terminal (2) and the output terminal (7) having a power amplification circuit (4) for amplifying the RF signal, a bypass (5) formed between the input terminal (2) and the output terminal (7) for the RF signal to bypass the amplification path (3), a control terminal (6) for controlling the operation of the amplification path (3) and the bypass (5) such that an RF signal is either passed through the amplification path (3) or the bypass (5). The power amplifier circuit (1') further comprises a variable gain amplifier circuit (15) for a pre-amplification of the input RF signal which is placed between the line from the input terminal (2) to the amplification path (3) and the bypass (5), and a delay

control means (16, 17, 18, 19) for controlling the variable gain amplifier (15), the amplification path (3), and the bypass (5), whereby, before setting the operating conditions of the variable gain amplifier (15), the amplification path (3), and the bypass (5) in a state to achieve the RF output power specified, the delay control means (16, 17, 18, 19) is adapted to first set the respective operating conditions in the inverse state thereof. The invention further relates to a method for executing the steps on this system.

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